



IR-2300 (2-3077)

In re Patent Application of:

Richard Bullock et al. Serial No.: 09/773,872 Filed: February 2, 2001

Date: October 28, 2002 Group Art Unit: 2812 Examiner: R.A. Booth

For: METHOD OF FABRICATING A GATE DIELECTRIC LAYER FOR A THIN FI TRANSISTOR

Asst. Commissioner for Patents Washington, D.C. 20231

PETITION AND FEE FOR AUTOMATIC EXTENSION OF TIME UNDER 37 CFR 1.17, 1.136(a) AND 35 USC 41(a)8

Sir:

Applicants hereby petition the Asst. Commissioner for Patents to extend the time for filing a response to the outstanding Office Action by 1 month. Enclosed is our check No. _///68 which includes the amount of \$110.00 for the petition fee in accordance with 37 CFR 1.17 computed as:

_xx___ Response within first month
_xx__ not small entity (\$110) ___ small entity (\$55)

You are authorized to charge to our Deposit Account No. 15-0700 any additional amounts owing.

If this petition is inadequate to avoid abandonment, the Assistant Commissioner for Patents is petitioned, under 37 C.F.R. §1.136(a), to extend the time by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on October 28, 2002

Samuel H. Weiner
Name of applicant, assignee or
Registered Representative

Signature

October 28, 2002
Date of Signature

SHW/BJK:gl

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Respectfully submitted,

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